

SPACE BYTE 8085 CPU

PRODUCT SUPPORT DOCUMENTATION

SPACE BYTE COMPUTER CORP.
6464 Sunset Blvd. Suite 530
Los Angeles, CA 90028
(213) 468-8080

COPYRIGHT DECLARATION

Any information hereunder contained, in the form of text, source listings, photographs, schematic or block diagrams, is the sole property of the SPACE BYTE CORP. and may not be reproduced or duplicated in any manner, or on any medium now known or yet to be devised without the written consent of the SPACE BYTE CORP.

© SPACE BYTE CORPORATION, 1977

ENGINEERING ERRATA

Caution: Always open iCOM drive doors prior to depressing reset or disk crash will result.

* * *

NOTE:

NEVER ATTEMPT TO INSERT OR REMOVE A CARD
FROM THE MOTHERBOARD WITH THE POWER TURNED ON.

* * *

SPACE BYTE 8085 CPU

Introduction and Design Philosophy

The design and marketing philosophy behind the development of the SPACE BYTE 8085 CPU was to create a single board computer containing all of the necessary I/O to support a disk based small business computer system. Additionally, the CPU card, with the optional SPACE BYTE 2708/2716 EPROM Programmer attached will function as the heart of a software development system- utilizing the CPU card as a self contained dedicated controller.

The ICOM FD3700, or frugal floppy with the FC-360 controller was chosen to interface directly with the SPACE BYTE 8085 CPU for the following reasons:

- 1) Simplicity of interface- connection directly to the CPU card, eliminating the need for the S-100 interface card (all software for disk I/O handling contained in the on board 3K system monitor.
- 2) Full size, 8" IBM compatible 256K diskette.
- 3) Full software support- both FDOS III relocatable assembler and operating system and DEBBI (Disk Extended Basic) are available from ICOM assembled for the SPACE BYTE 8085 CPU hardware configuration. Absolutely no patching is necessary.
- 4) ICOM products are well supported at the retail and OEM levels.

With this philosophy in mind, the SPACE BYTE 8085 CPU was further developed to allow more versatility. This ofcourse being the inclusion of the necessary routines to support the Tarbell cassette medium and Polymorphic Video Interface, enabling the use of the Electric Pencil, by Michael Shroyer for complete word processor capability. A variety of jumper schemes allow such options as relocatable system PROM address (0000, C000, or E000), use of the INTEL 2716 EPROM, for up to 6K of onboard application firmware and pin 4 & 5 clear to send function for serial ports and M-WRITE generation, through the bus.

Careful review of the following information will assure complete utilization of the SPACE BYTE 8085 CPU.